

F I G. 1

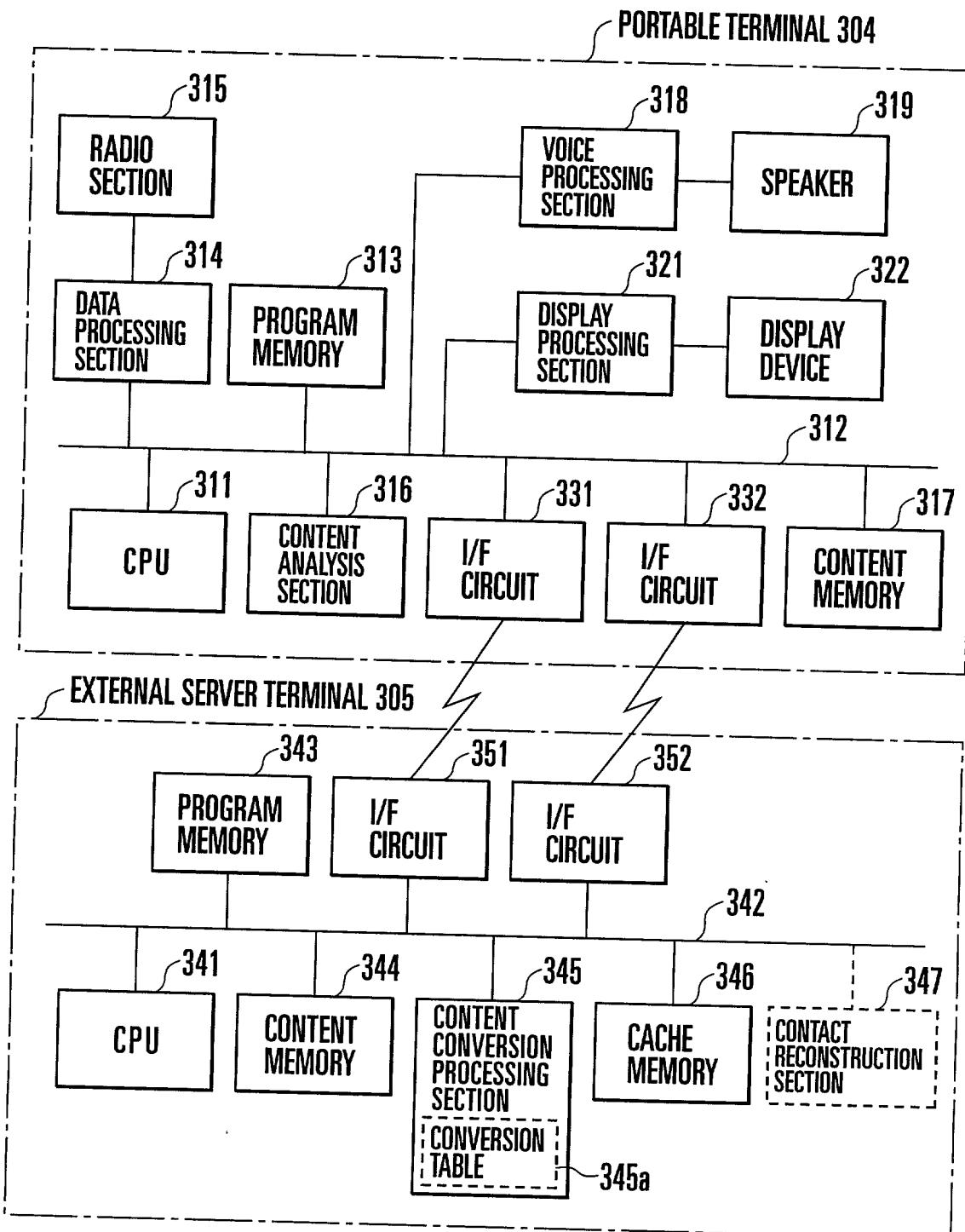
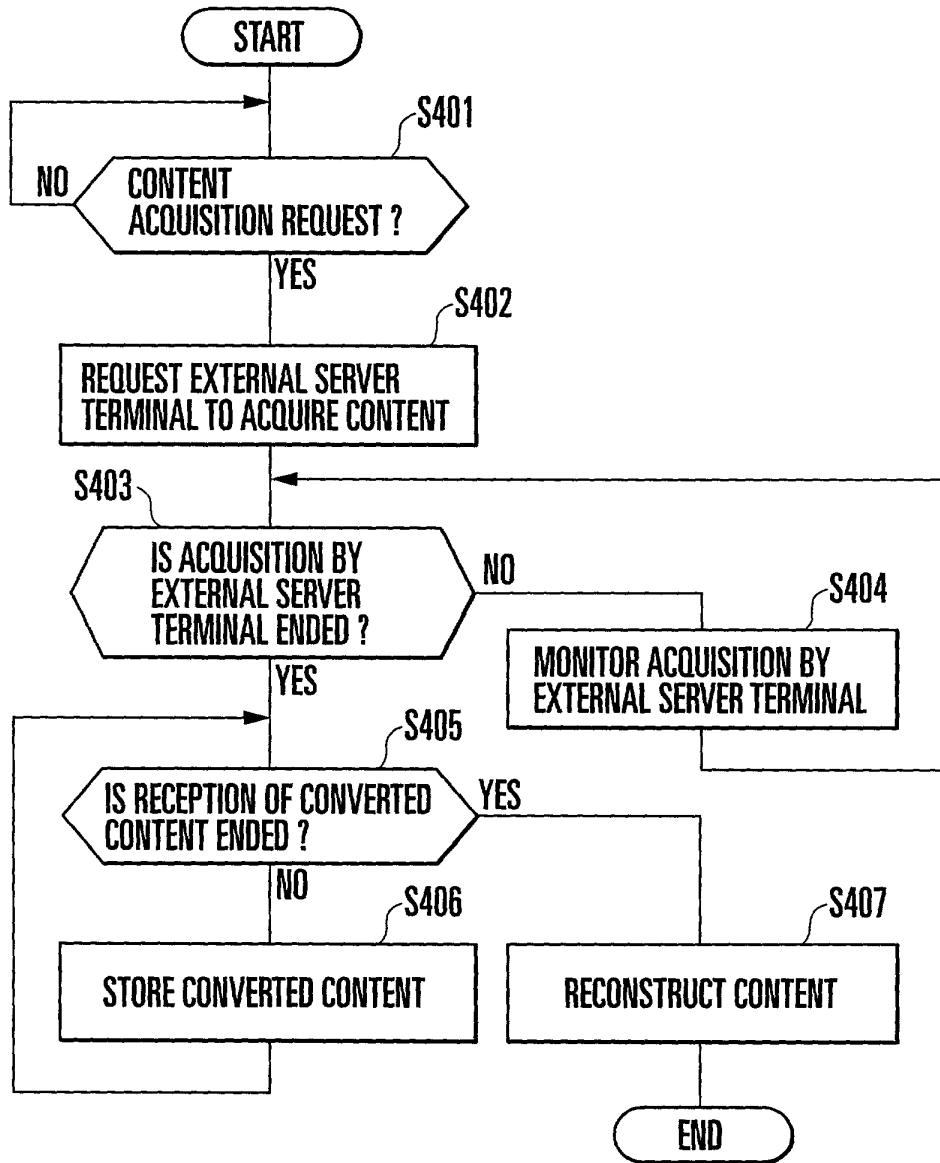


FIG. 2



F I G . 3

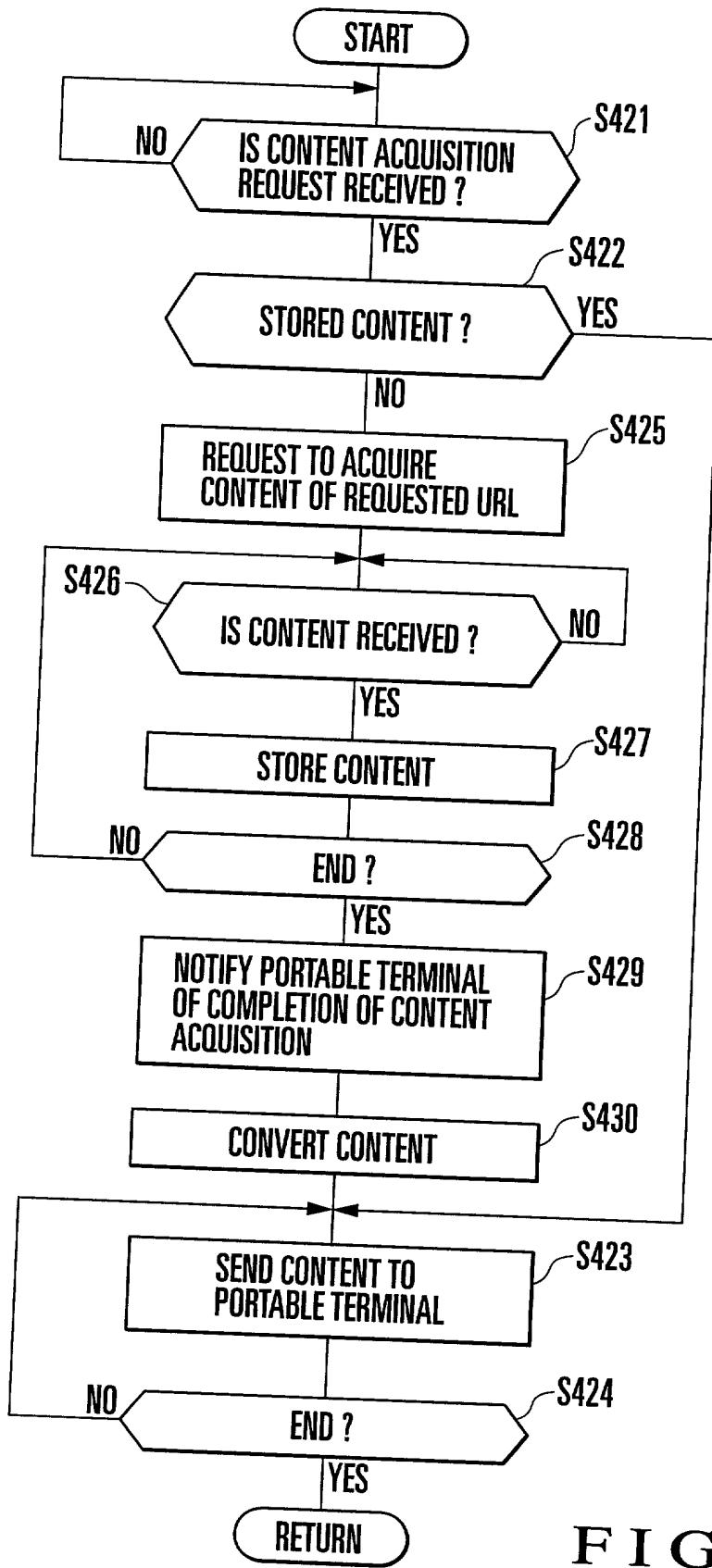
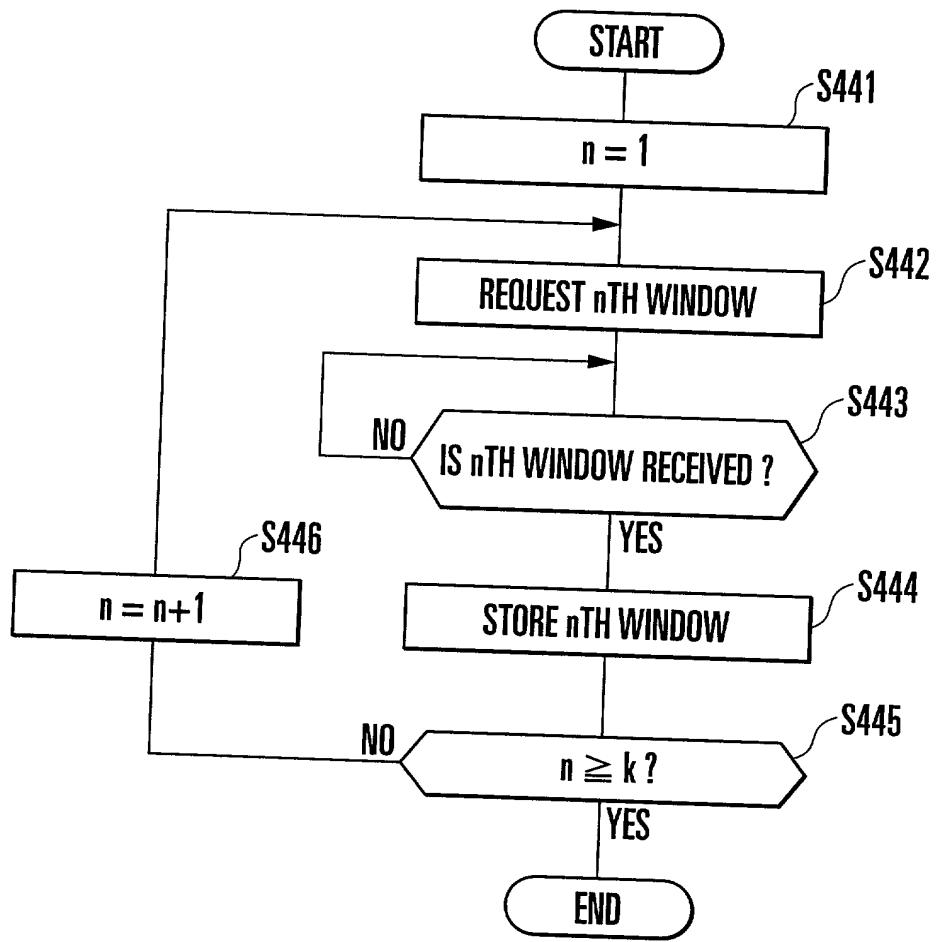


FIG. 4



F I G. 5

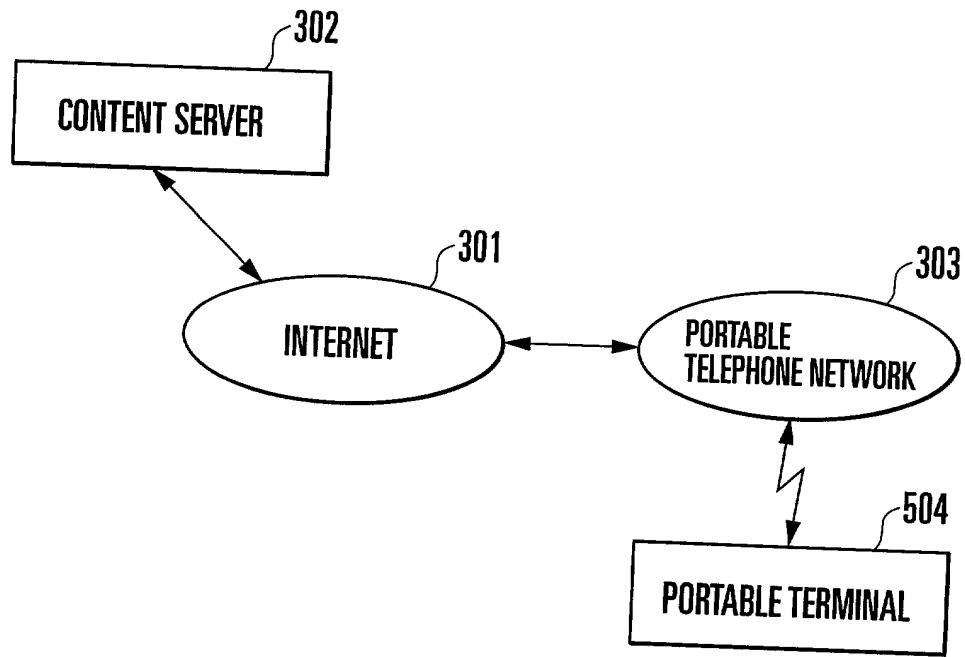


FIG. 6A

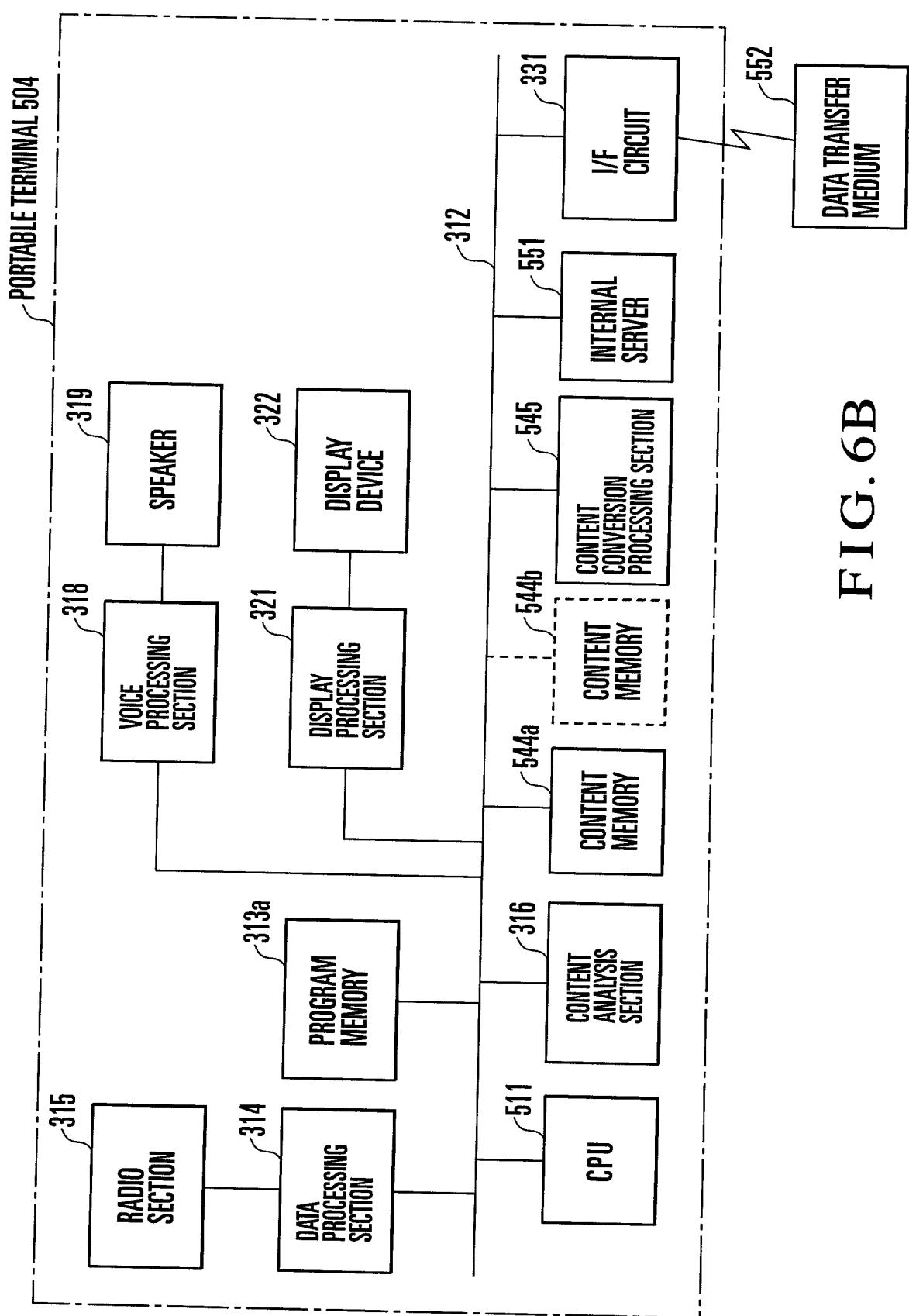


FIG. 6B

FIGURE 7  
Sequence of Events in Data Flow Between Content Server 302, CPU 511, Display Processing Section 321, and Internal Server 551

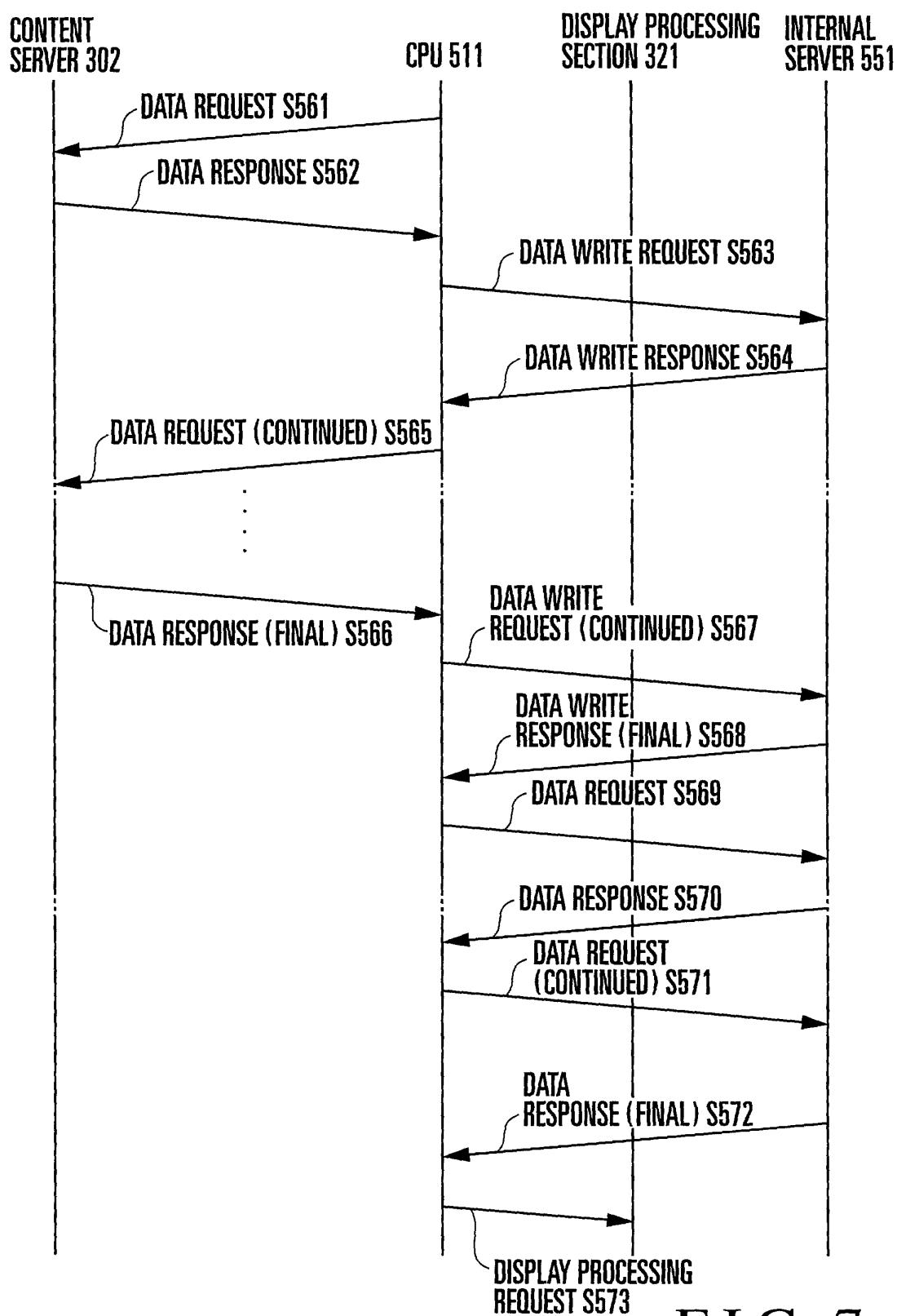
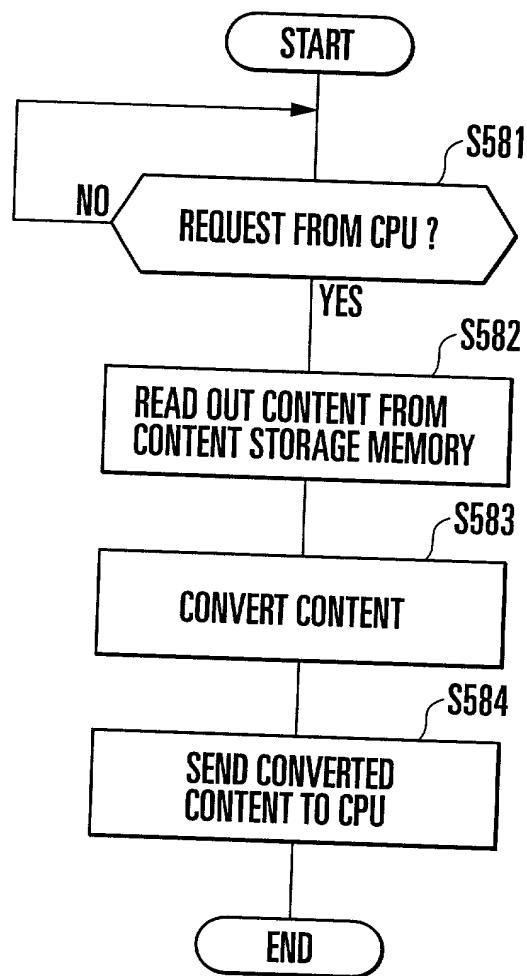
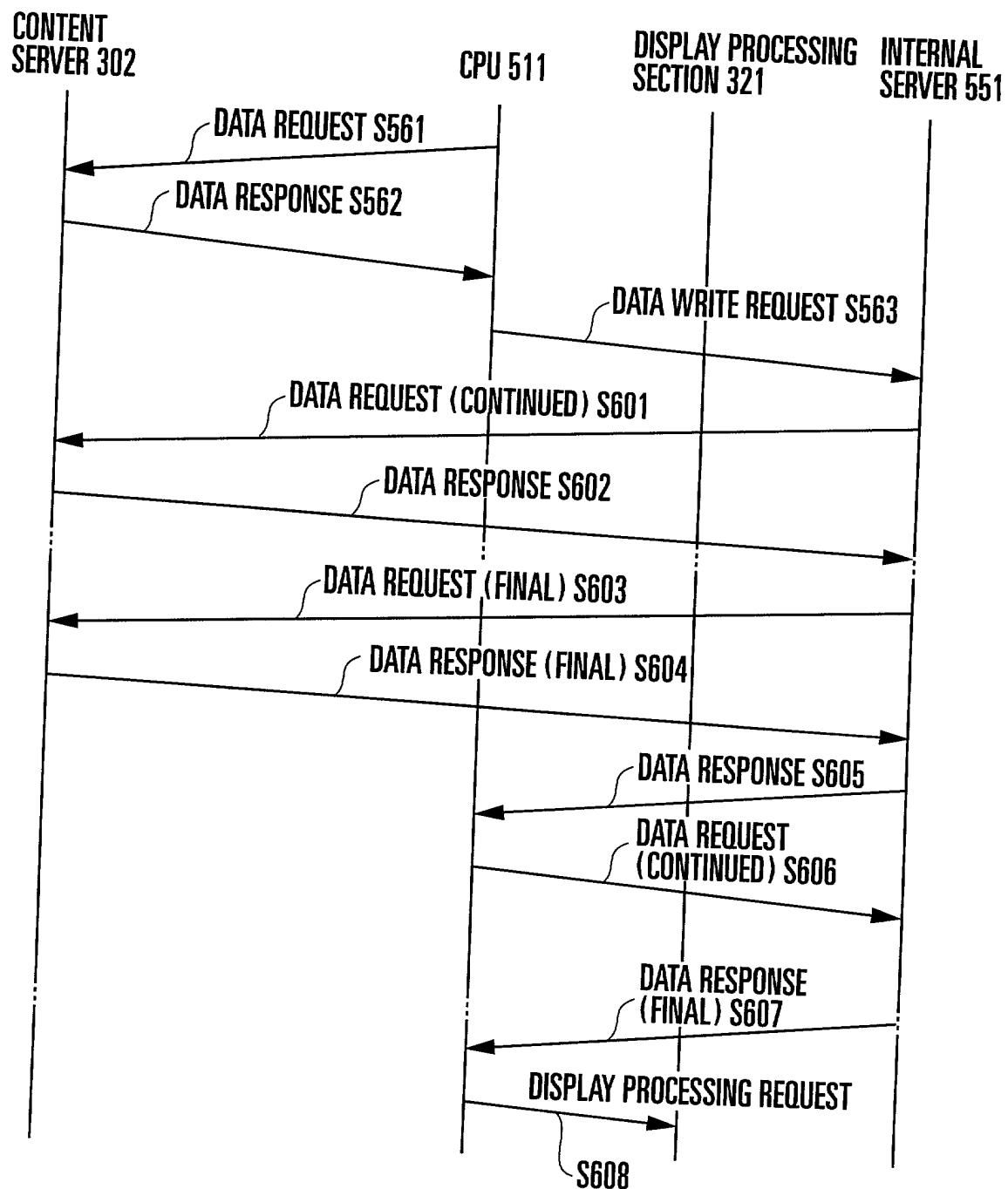


FIG. 7



F I G. 8



F I G . 9

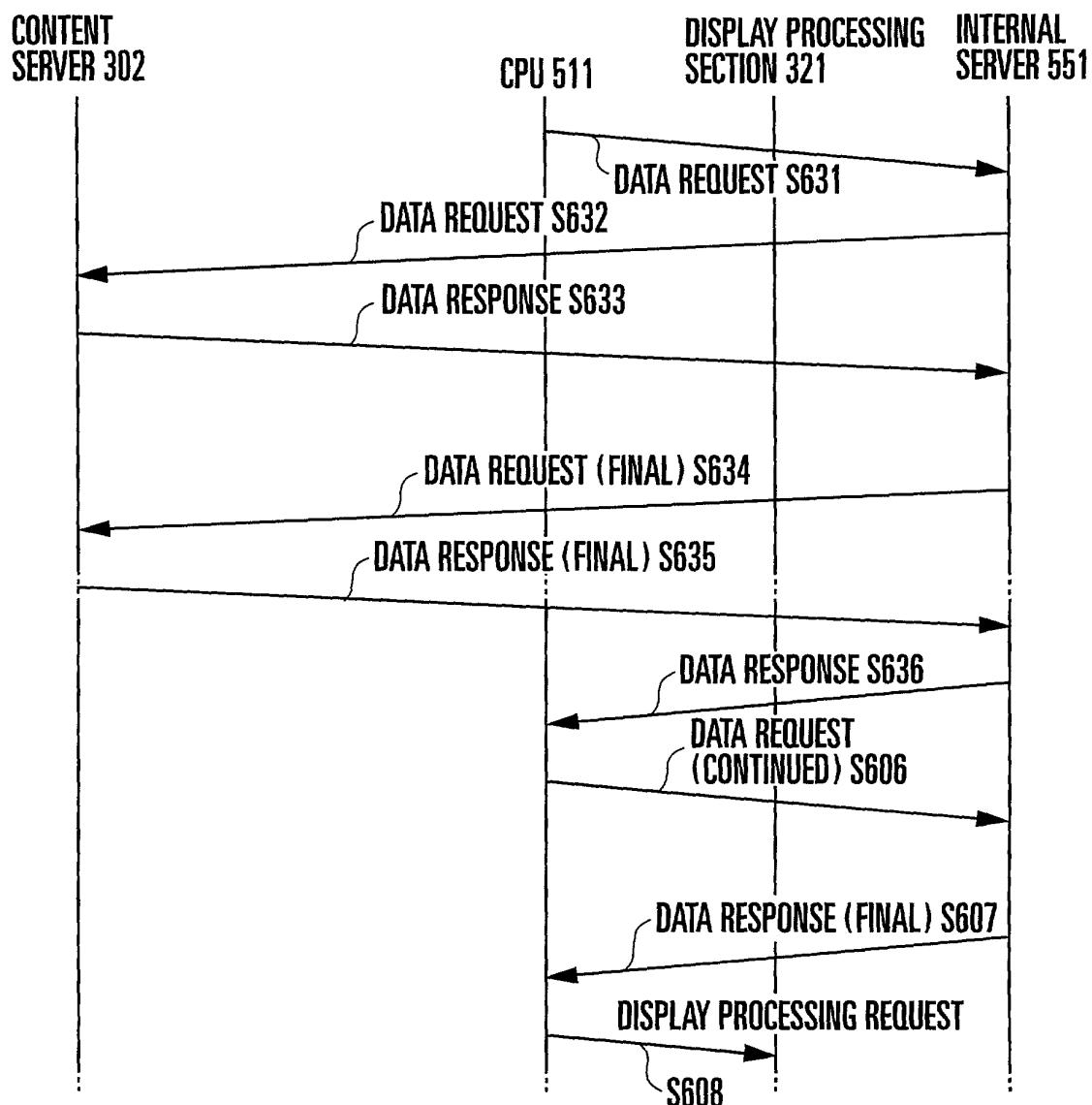


FIG. 10

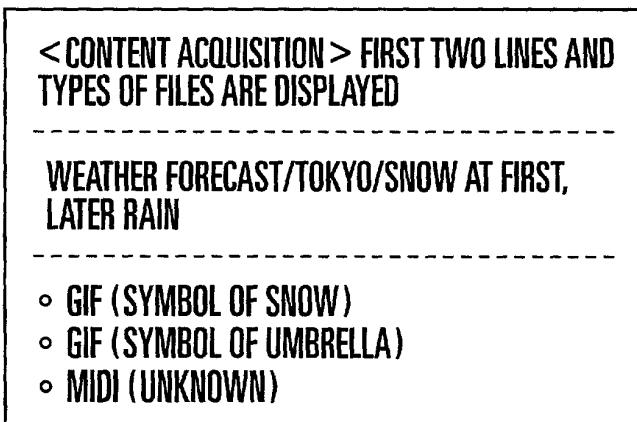
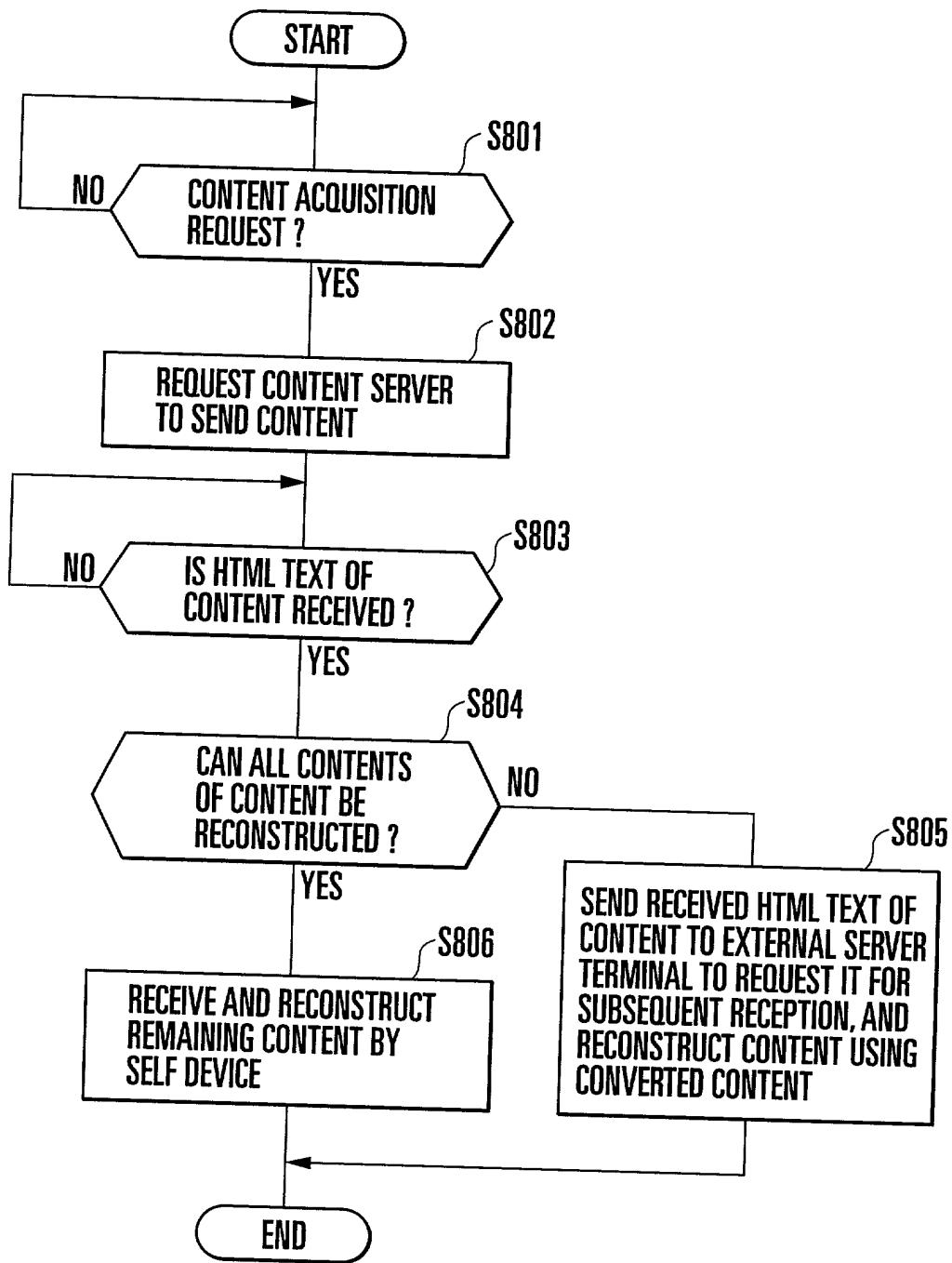
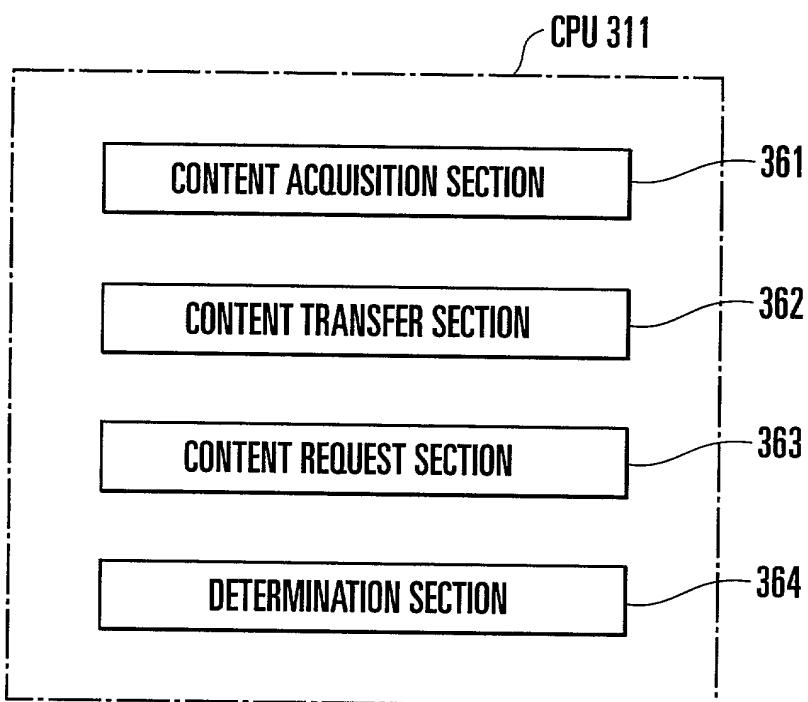


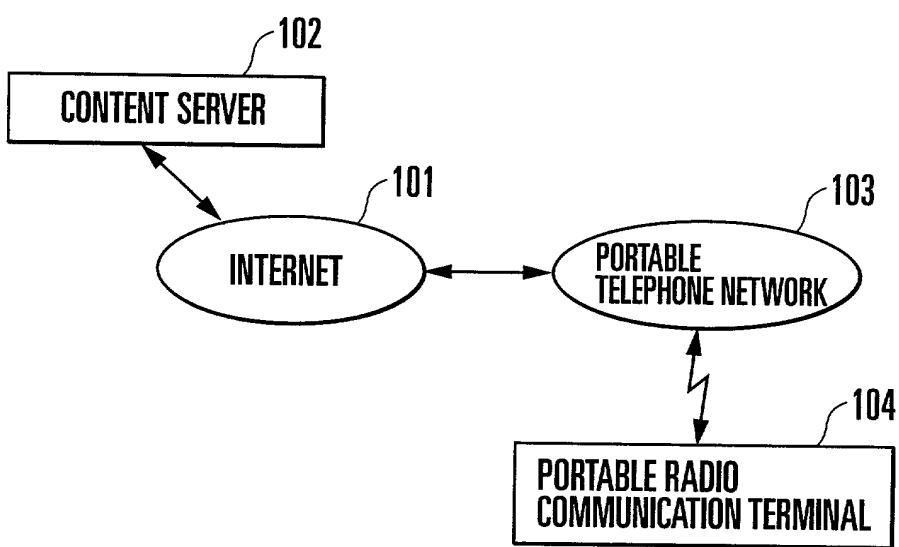
FIG. 11



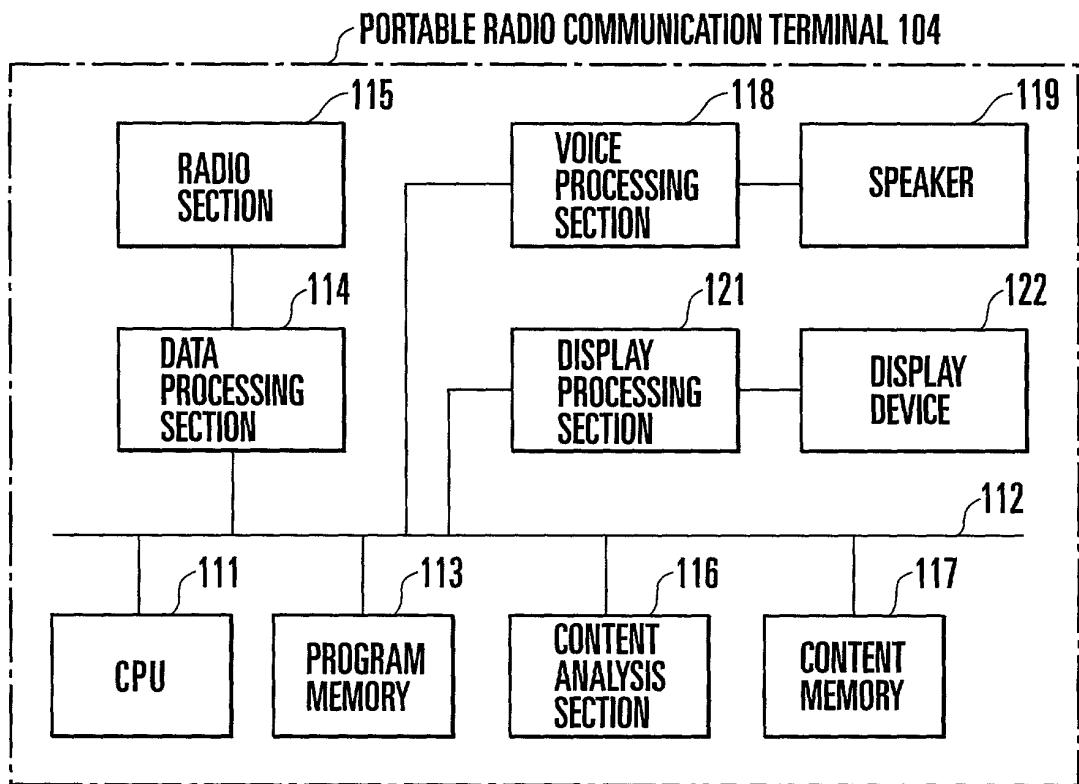
**F I G. 12**



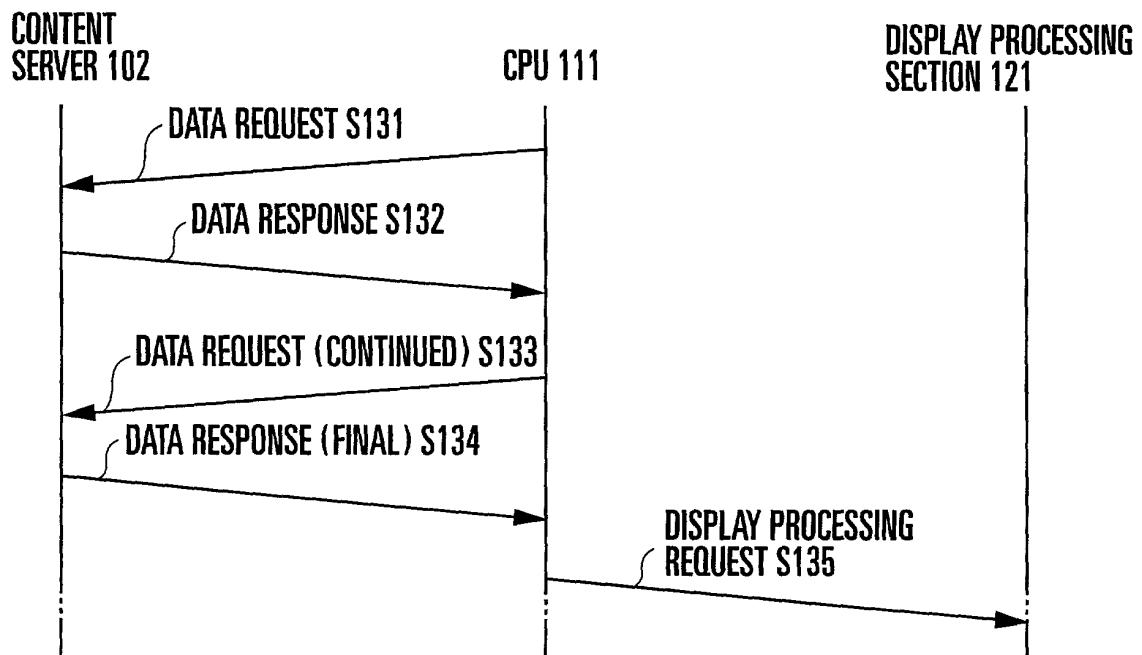
**FIG. 13**



**FIG. 14**  
**PRIOR ART**



**F I G. 15**



**F I G. 16  
PRIOR ART**

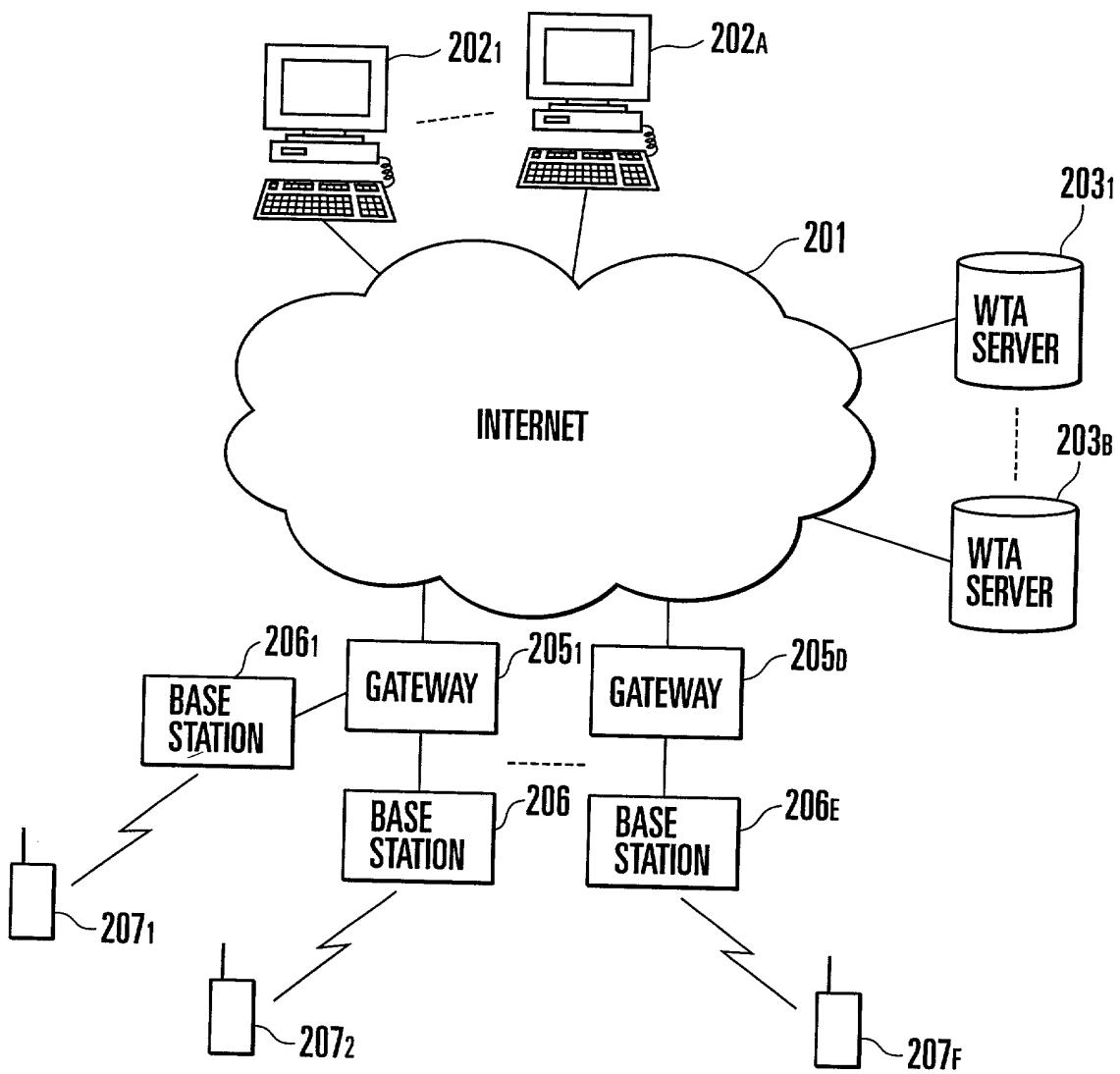
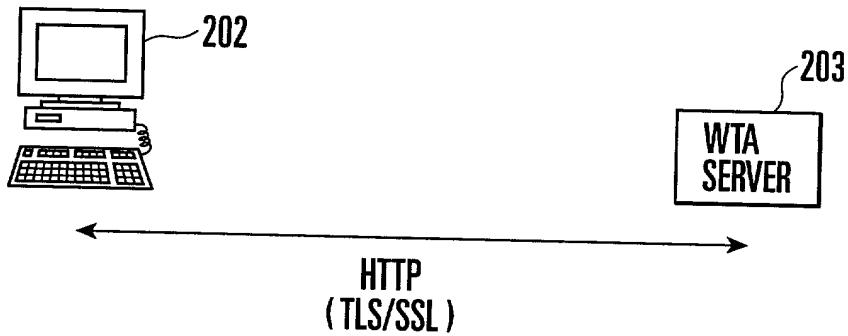
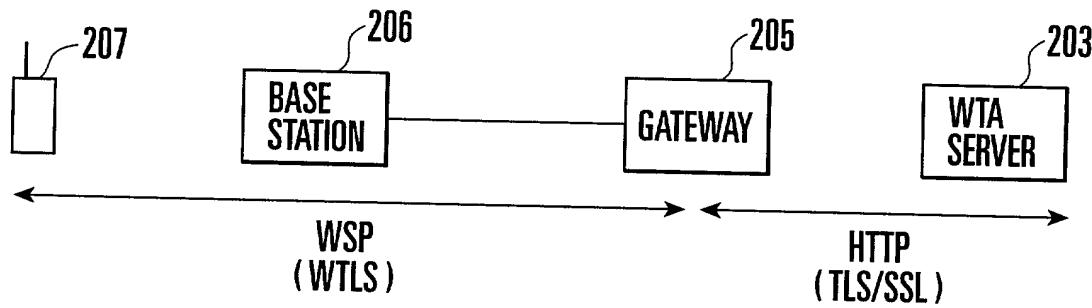


FIG. 17  
PRIOR ART



**F I G. 18**



**F I G. 19**